

TCXO & VCTCXO

STA, STC, STD

- Temperature Compensated Crystal Oscillator
- HCMOS / TTL output
- Clipped Sinewave Output
- 3.3V / 5.0V supply voltage

STA



STC



STD



DIP TYPE

ELECTRICAL SPECIFICATIONS

	TTL / HCMOS	CLIPPED SINEWAVE
Frequency Range	1.200 to 40.000MHz	9.600 to 40.000MHz
Load Drive Capability	10 TTL Load or 15pF HCMOS Load(STD)	10kOhms//10pF
Output Voltage Logic High(V_{OH})	w/TTL : 2.4V _{DC} min. w/HCMOS : 90% of V_{DD} min.	0.8Vp-p min.(V_{DD} : 3.3V _{DC})
Output Voltage Logic Low(V_{OL})	w/TTL : 0.4V _{DC} max. w/HCMOS : 10% of V_{DD} max.	1.0Vp-p min.(V_{DD} : 5.0V _{DC})
Frequency / Stability	vs. Operating Temperature Range vs. Input Voltage ($\pm 5\%$) vs. Load ($\pm 10\%$)	See Table 2 ± 0.3 ppm max. ± 0.3 ppm max.
Supply Voltage(V_{DD})	3.3V _{DC} $\pm 5\%$, 5.0V _{DC} $\pm 5\%$	3.3V _{DC} $\pm 5\%$, 5.0V _{DC} $\pm 5\%$
Input Current	1.200 to 27.000MHz 20mA max. 25mA max. 27.001 to 40.000MHz 35mA max. 40mA max.	9.600 to 27.000MHz 2mA max. 3mA max. 27.001 to 40.000MHz 3mA max. 4mA max.
Rise / Fall Time	4ns max. 5ns max.	
Duty Cycle	50 $\pm 10\%$	
Internal Trim (Top of Can)		± 3 ppm min.
Control Voltage(External)	1.65V _{DC} ± 1.65 V _{DC} (V_{DD} : 3.3V _{DC}), 2.5V _{DC} ± 2.0 V _{DC} (V_{DD} : 5.0V _{DC}) Positive transfer Characteristic	
Frequency Deviation	± 5 ppm or ± 10 ppm minimum Over Control Voltage	
Aging (at 25°C)	± 1 ppm/year max.	

MECHANICAL DIMENSIONS

